

AMENDMENTS TO THE CLAIMS

1.-7. (Canceled) .

8. (Previously Presented) A microcomputer comprising:
a program for operating the microcomputer;
a plurality of peripheral circuits, each having an individual
function and functioning interactively with each other; and
a connecting circuit connected to the peripheral circuits and
comprising a plurality of selectors that select one from among a
plurality of input signals and then output the selected input
signal to the peripheral circuits according to data given by the
program,
wherein the plurality of peripheral circuits includes an
output register that, according to commands included in the
program, latches and stores the data temporarily, and thereafter
outputs the data to the connecting circuit;
a predetermined selector is selected by the program and the
data is fed from the output register to the selected selector so
that a signal to be outputted to the peripheral circuits is
selected from among the plurality of input signals; and
thereby, interconnection among the plurality of peripheral
circuits is changed.

9. (Previously Presented) A microcomputer as claimed in claim 8,

wherein the plurality of peripheral circuits includes an input register that, according to the commands included in the program, latches and stores temporarily the data to be outputted from the peripheral circuits and thereafter outputs the data so that the data is taken into the microcomputer.

10. (Previously Presented) A microcomputer as claimed in claim 8,

wherein the plurality of peripheral circuits includes a logic circuit for inputting an external signal to the peripheral circuits.

11. (Previously Presented) A microcomputer as claimed in claim 8,

wherein the plurality of peripheral circuits includes a logic circuit for outputting externally a signal generated within the peripheral circuits.

12. (Previously Presented) A microcomputer as claimed in claim 10,

wherein the peripheral circuits to which the external signal is inputted is a timer for measuring periods for which the external signal remains at a high level and a low level respectively.

13. (Previously Presented) A microcomputer as claimed in claim 10,

wherein the peripheral circuits to which the external signal is inputted include a first timer that starts counting on a trailing edge of the external signal and stops counting on a rising edge thereof subsequent to the trailing edge and a second timer that starts counting on a rising edge of the external signal and stops counting on a trailing edge thereof subsequent to the rising edge.

14. (Previously Presented) A microcomputer as claimed in claim 11,

wherein the plurality of peripheral circuits for generating the signal to be outputted externally includes a first timer and a second timer and generates a signal having a high level for a predetermined period and a low level for another predetermined period by changing said interconnection by way of the connecting circuit.